

CAPACITIVE LOAD DRIVING CIRCUIT DRIVING CAPACITIVE LOADS
SUCH AS PIXELS IN PLASMA DISPLAY PANELS AND PLASMA
DISPLAY APPARATUS HAVING THE CAPACITIVE LOAD DRIVING
CIRCUIT

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ABSTRACT OF THE DISCLOSURE

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A capacitive load driving circuit has an input
terminal, a front-edge delay circuit, a back-edge delay
circuit, an amplifying circuit, and an output switch
device driven by the amplifying circuit. The front-edge
15 delay circuit delays a front edge of an input signal
input via the input terminal, the back-edge delay circuit
delays a back edge of the input signal, and the
amplifying circuit amplifies a drive control signal
obtained through the front-edge delay circuit and the
20 back-edge delay circuit.